CS208
Monday
31 Jan 2022
Things that would help to know
- typical structures (loops, etc.)
- stuff you can ignore
- conventions
- rax, eax, ax, ah, al
- lea
\[
\begin{align*}
\text{eax} & \quad \text{32-bit} \quad \text{(\sim 1980)} \\
\text{rax} & \quad \text{64-bit} \\
\text{ah} & \quad 8 \\
\text{al} & \quad 8
\end{align*}
\]
ADD
JNZ J2, SLE
JFE J2, SLE
JSF, ZF
JCF
MOV
relevant flags
by bits
The x86
I want you instructions

Heart
to know

May

Test?
This example was a bit (ha!) of a mess